

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil K. Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA
EFFECT OF A CMOS BIPOLAR TRANSISTOR

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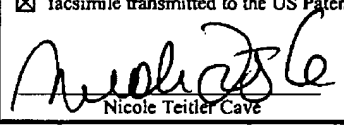
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Respectfully submitted,



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